

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination ROYER ET AL.	
		Examiner Leonid Kravets	Art Unit 2189	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,732,238	03-1998	Sarkozy, Andras	711/135
	B	US-5,937,433	08-1999	Lee et al.	711/158
	C	US-6,629,198	09-2003	Howard et al.	711/112
	D	US-5,603,331	02-1997	Heemels et al.	600/508
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Handy, Jim, the Cache Memory book, 1998, Academic Press, 2 nd Edition, page xv
	V	Kozierok, Charles, PC Guide - Logical Block Addressing, 4/17/2001, http://www.pcguide.com/ref/hdd/bios/modesLBA-c.html
	W	Kozierok, Charles, PC Guide - BIOS Functions and Operation, 4/17/2001, http://www.pcguide.com/ref/mbsys/bios/func.htm
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.